



The EnCore Embedded Processor

Silicon Proven

Feature	EnCore 130nm [speed opt]	ARM Cortex M3 130nm [speed opt]
Core Size [Baseline]	0.15 mm ²	0.43 mm ²
Dhrystone MIPS/MHz	1.45 [Single Issue]	1.25 [Single Issue]
EEMBC CoreMark™/MHz	2.040 [Single Issue]	1.753 [Single Issue]
Power Consumption [250 MHz]	0.070 mW/MHz [FMAX 250 MHz]	0.140 mW/MHz [FMAX 135 MHz]



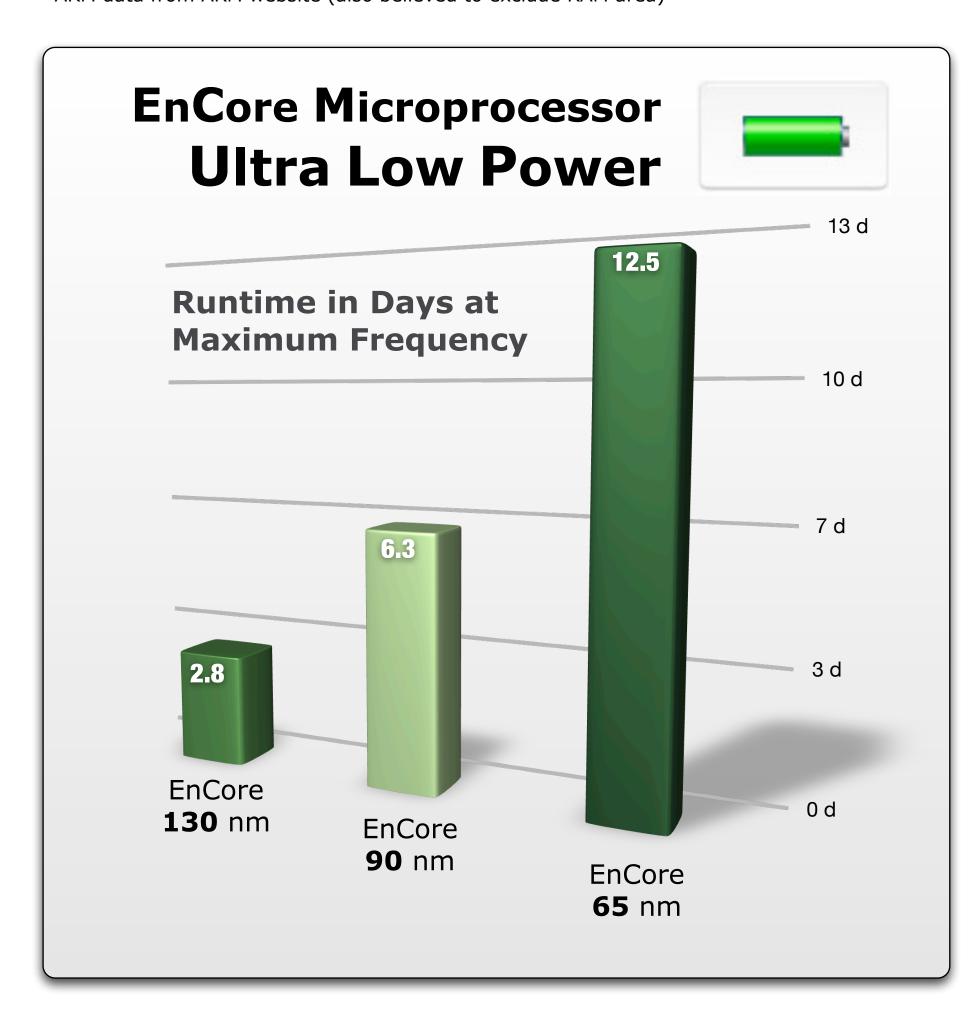
Feature	EnCore 90nm [speed opt]	ARM Cortex M3 90nm [speed opt]
Core Size [Baseline]	0.071 mm ²	0.210 mm ²
Dhrystone MIPS/MHz	1.45 [Single Issue]	1.25 [Single Issue]
EEMBC CoreMark™/MHz	2.040 [Single Issue]	1.753 [Single Issue]
Power Consumption [250 MHz]	0.024 mW/MHz [FMAX 420 MHz]	0.070 mW/MHz [FMAX 191 MHz]

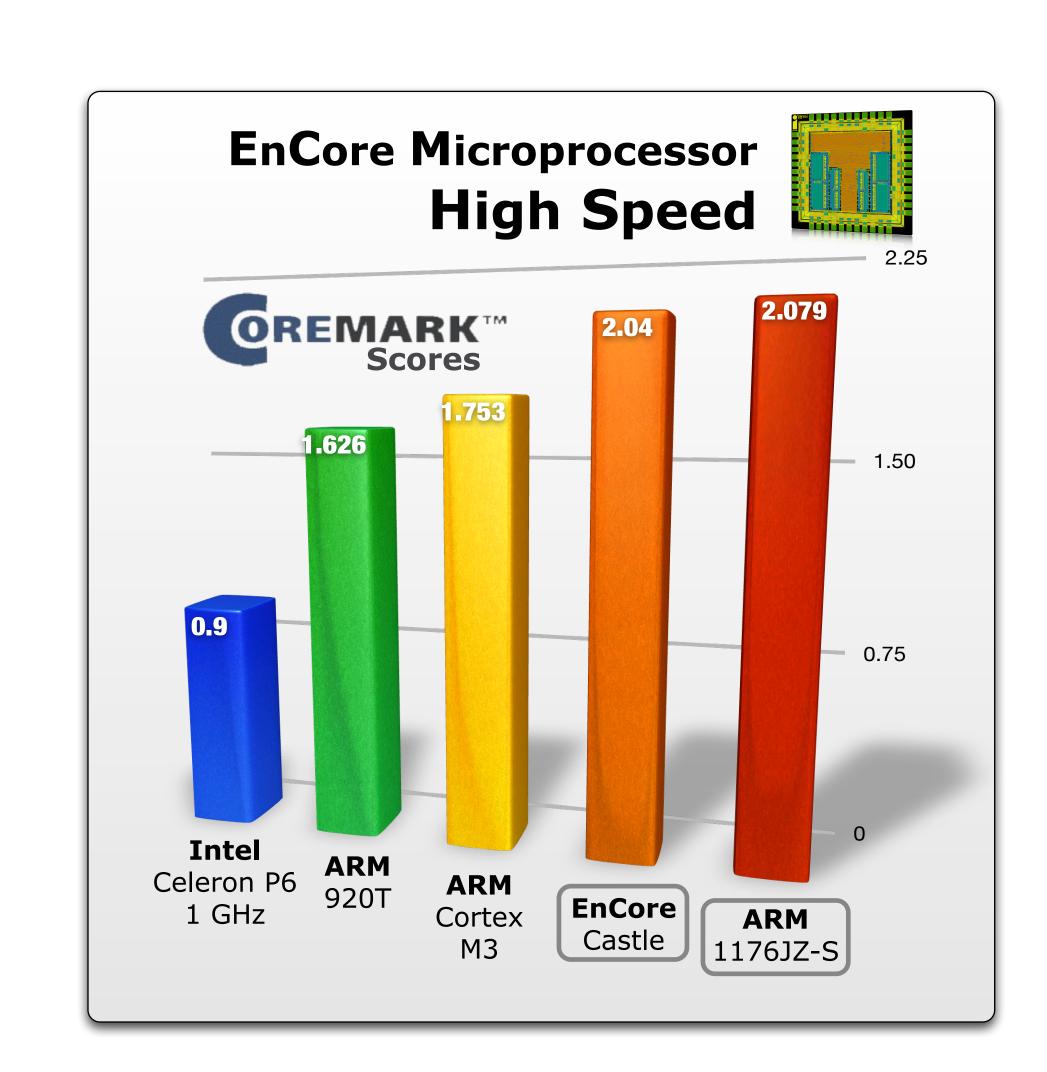


Feature	EnCore 65nm [low power]	ARM Cortex M3 65nm
Core Size [Baseline]	0.036 mm ²	N/A
Dhrystone MIPS/MHz	1.45 [Single Issue]	1.25 [Single Issue]
E EMBC C ore M ark [™] / M Hz	2.040 [Single Issue]	1.753 [Single Issue]
Power Consumption [250 MHz]	0.012 mW/MHz [FMAX 500 MHz]	N/A

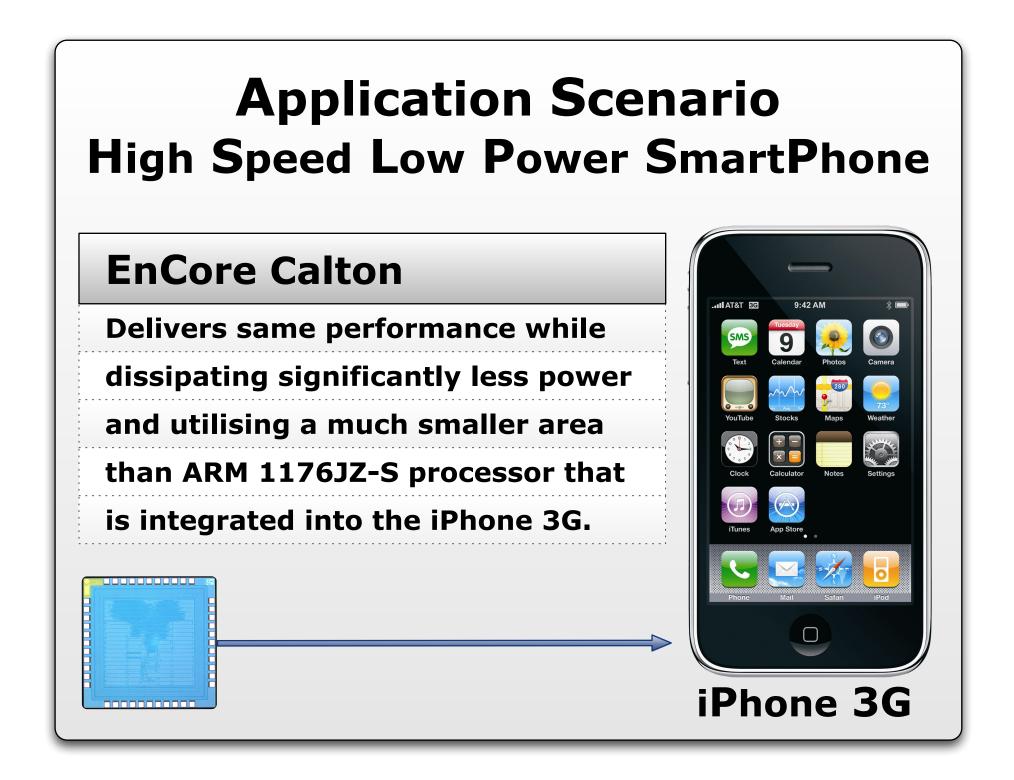
- Core sizes exclude RAM area

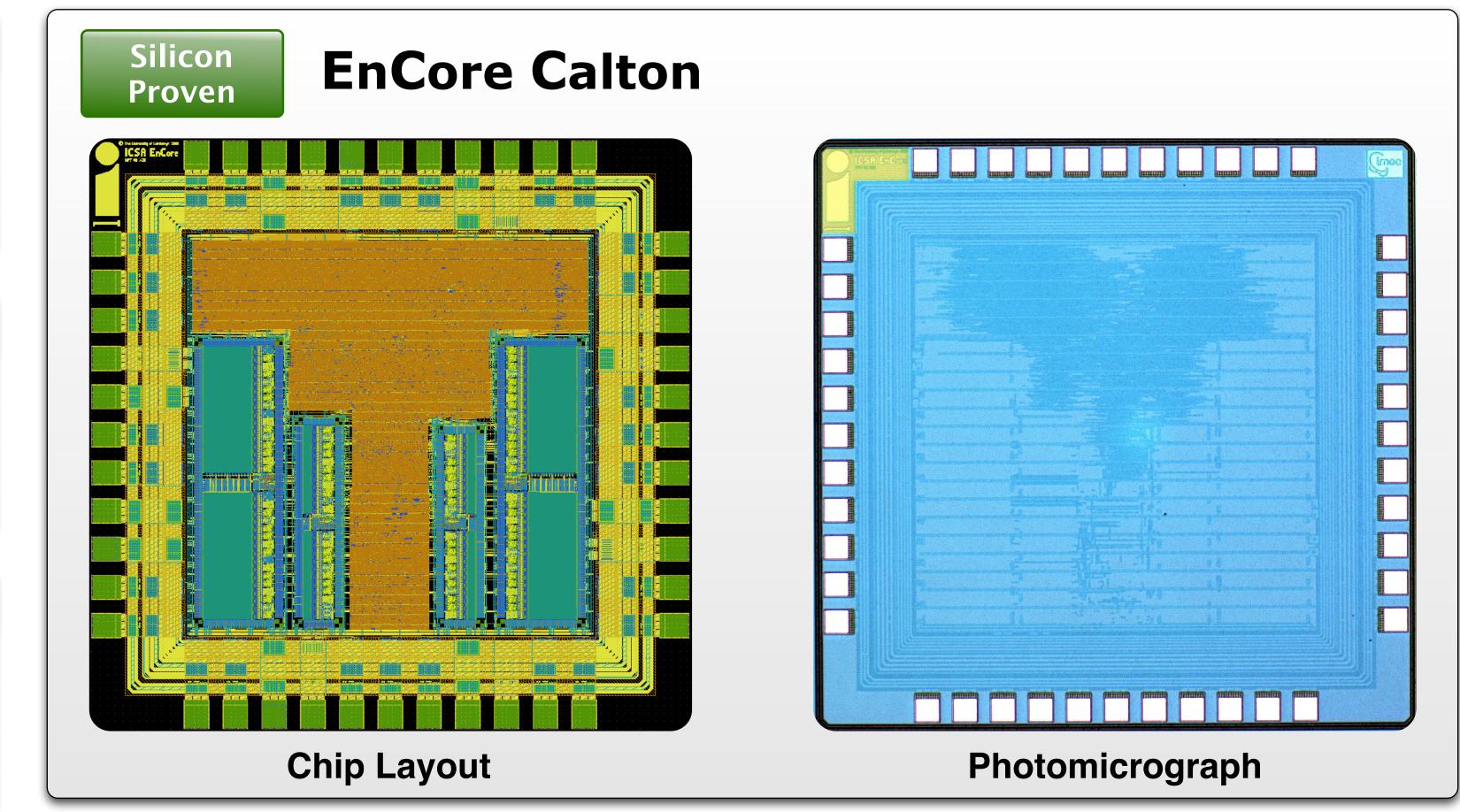
- EnCore power measurements include cache RAM power - ARM data from ARM website (also believed to exclude RAM area)

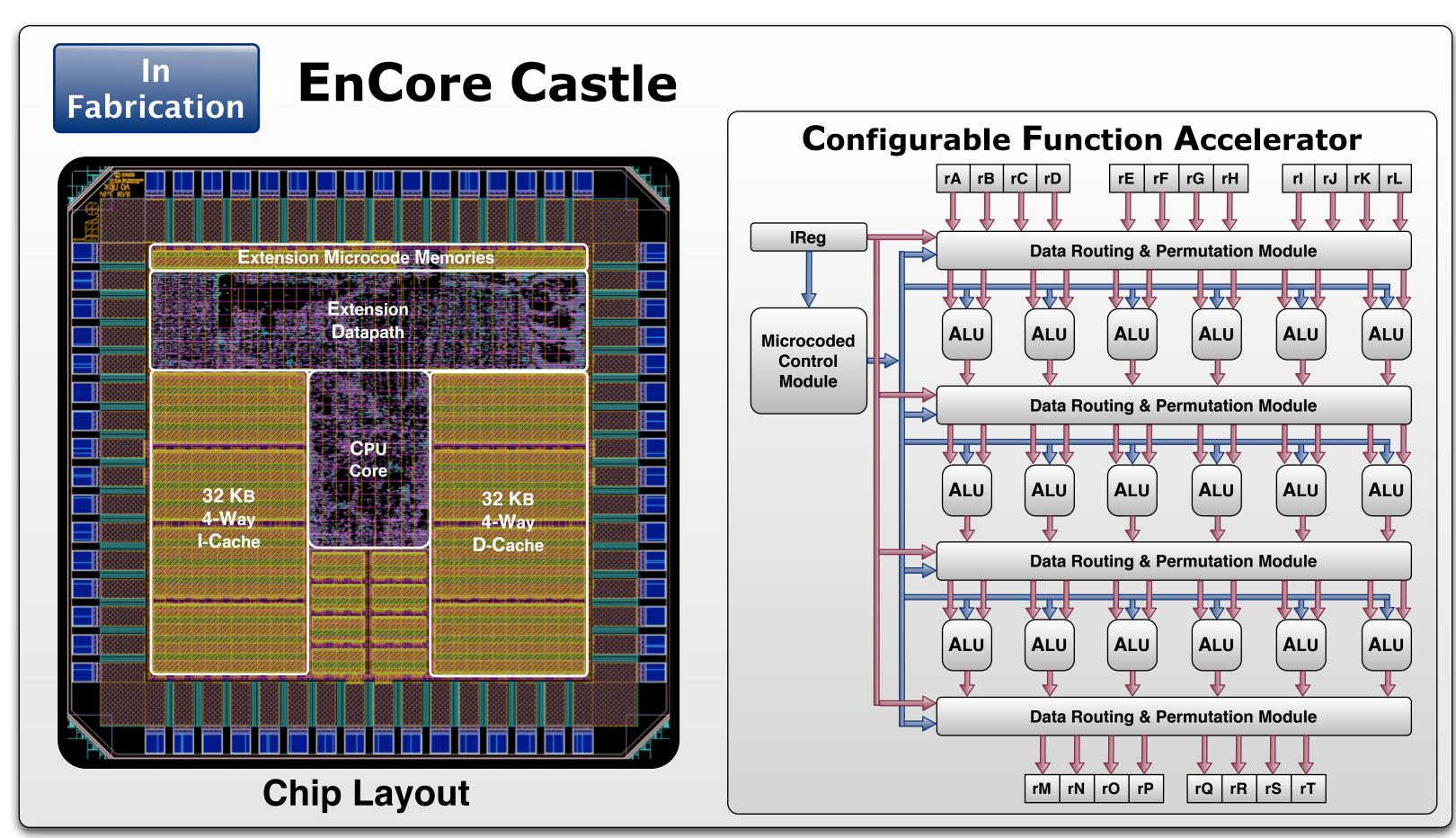


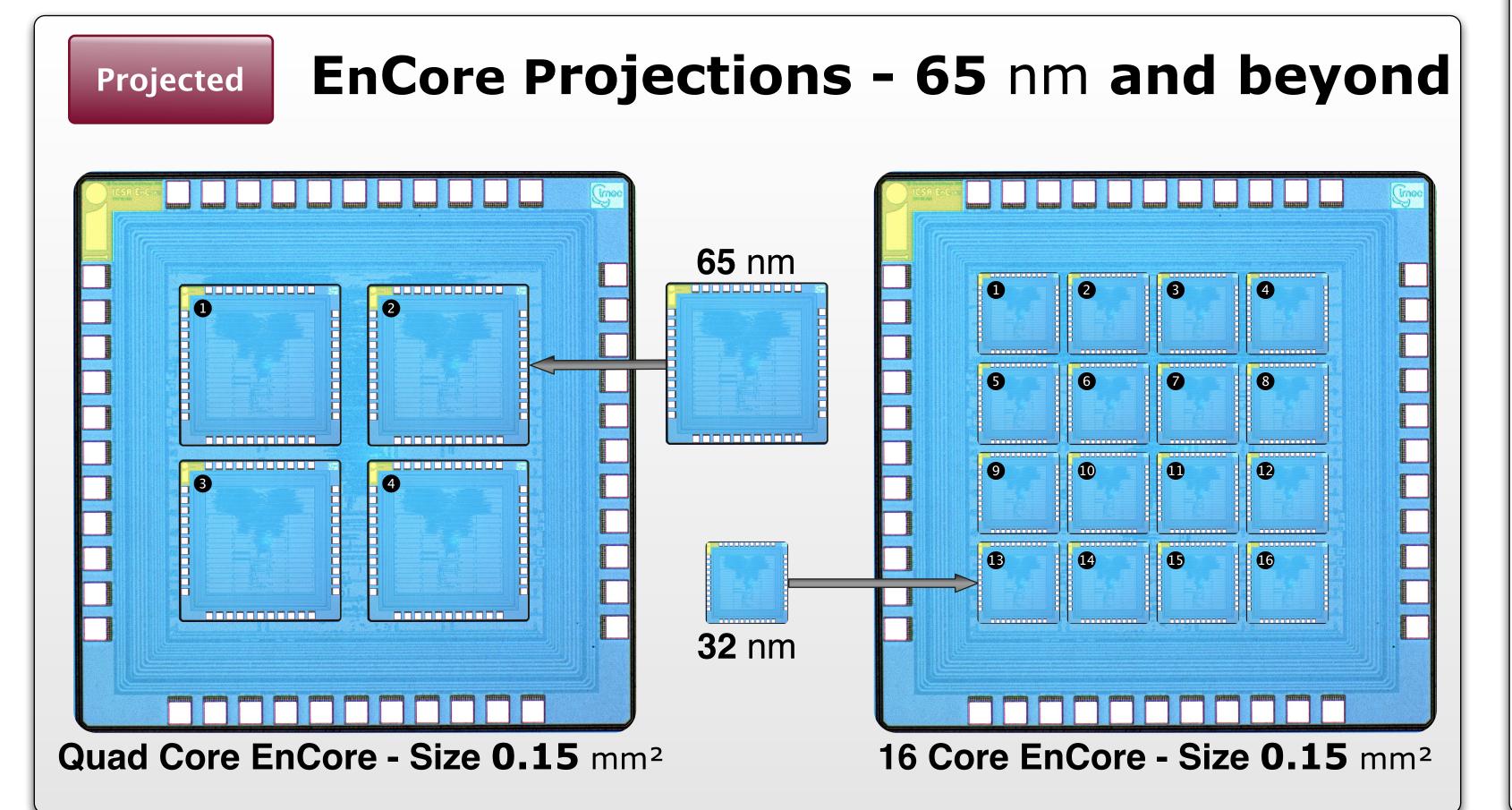




















Marcela



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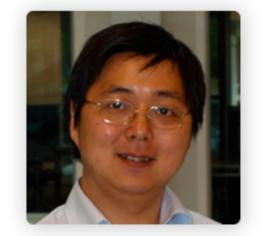




Oscar



Georgios



Freddie



